TEQIP-II SPONSORED

Short Term Course
On
“Recent Trends in VLSI and Communication System” (RTVCS - 13)
June 17-21, 2013

Patron:
Prof. (Dr.) S. K. Das
Director NIT Jalandhar

Chief Coordinator:
Prof. (Dr.) R K Sarin.
Dr. A K Khosla.

Coordinators:
Er. Ashish Raman.
Er. Deepti Kakkar.
Dr. Mamta Khosla.

Organized by:
Department of Electronics and Communication Engineering, Dr B R Ambedkar National Institute of Technology, Jalandhar - 144011, Punjab
www.nitj.ac.in

About the Institute

Dr B R Ambedkar National Institute of Technology (NIT), Jalandhar was established in the year 1987 as Regional Engineering College, was given the status of National Institute of Technology in the year 2002, and functions under the aegis of Ministry of Human Resource development (MHRD), Government of India. As an Institute of National Importance, it imparts high quality technical education in Engineering, Technology and Science to produce competent technical manpower for the country.

Location

The institute is located on Amritsar bye pass at a distance of 12 km from Jalandhar bus stand, 11 km from Jalandhar city railway station, 16 km from Jalandhar Cantt, railway station, 170 km from Chandigarh, 80 km from Raja Sansi International Airport, Amritsar and 375 km from Indira Gandhi International Airport, New Delhi.

About the Department

The Department of Electronics and Communication Engineering offers B.Tech in Electronics and Communication Engineering, two full time M.Tech programs in Electronics and Communication Engineering and VLSI Design. Department also offers the Doctoral programmes in the field of Communication, VLSI Design and Bio-Medical Engineering. The department has established state-of-art laboratories with sophisticated equipments for undergraduate, post graduate and Ph.D research work. The department has an advanced VLSI lab sponsored by Ministry of Communication and IT under SMDP-II project. In the VLSI lab Cadance, Mentor graphic, Synopsys, Magma, Coware and Xilinx softwares are available for synthesis/simulation work.

Objective of Course

Very Large Scale Integrated (VLSI) Circuit is a leading field to design of modern Electronic and Communication Systems. There is great need for manpower training in the area of VLSI and Communication System Design. The main objective of this course is to provide the theoretical and practical knowledge of the recent trends available in the domain of VLSI and Communication Systems. During this programme the academicians and industry personnel in their respective areas of expertise will discuss the current scenario on analog and digital VLSI. In depth knowledge of upcoming trends in communication systems will also be delivered by experts. This course gives an opportunity for hands-on experience on the VLSI design based CAD tools and wireless communications system tools.

General Information:
• The candidate will be informed of his/her selection in advance.
• Shortlisted candidates shall be called for Course.
• Numbers of seats are limited.
• No course fee will be charged from the participants.
• Course material, lunch and refreshment will be provided during the workshop.
• No TA/DA will be paid by the institute for attending the course.
• The accommodation of outstation participants will be provided in hostel on sharing basis at nominal charges, (on request).
• Last date of receiving of registration forms: 12.06.2013.
REGISTRATION FORM
TEQIP-II Sponsored
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Name:  
Designation:  
Age:  
Male/Female:  
Academic Qualification:  
Specialization:  
Teaching Experience:  
Department:  
Name of Organization and Address:  

Accommodation required:  

Signature & Date  
Mobile no.:  
Email:  

RECOMMENDATION OF THE SPONSORING AUTHORITY  

The applicant is hereby sponsored and will be permitted to attend the workshop on “Recent Trends in VLSI and Communication Systems”, if selected.  

Signature and Seal of Sponsoring Authority with Date  

Important Dates  
Reporting Date and Time at the Institute:  
Date: 17/06/2013 at 9.00 am.  
Venue: NIT Jalandhar  

*The complete registration form should reach by speed post or email latest by June, 12, 2013 at the following address:  

Corresponding Address:  
Course Coordinators  

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*The registration form and information can be downloaded from the institute website, www.nitj.ac.in