



DR B R Ambedkar National Institute of Technology
(Institute of National Importance)
Department of Electronics & Communication Engineering
G T Road Bye pass, Jalandhar -144011, Punjab (India)

Ref No.:NITJ/ECE/

Date:

Advertisement No.

Walk in interview for the following position in the Department of Electronics and Communication Engineering under the project SMDP-C2SD in VLSI Design and related Software sanctioned by Ministry of Communication and Information Technology, Government of India, will be held as per details given below.

Sr No	Post	Qualification	Experience	Desirable	Duration	Consolidated Salary/Month
1	Lab Engineer on Temporary basis (01 Post)	1 st class B.Tech in Electronics Engg. and 1 st class M.Tech or equivalent degree with background in VLSI/ Microelectronics /relevant areas	Nil	Expertise in the designing of VCO and Mixer Schematic, Layout and parameters extractions.	Till the competition of the Project (Likely to be completed till Dec 2019)	Initial Salary is Rs. 34000/- for 1 st year. Enhancement in salary as per guidelines of project.
2	Project/Research Associate on Temporary basis (01 Post)	1 st class B.Tech in Electronics Engg. and 1 st class M.Tech or equivalent degree with background in VLSI/ Microelectronics/ relevant areas	Nil	Expertise in the designing of VCO and Mixer Schematic, Layout and parameters extractions.	Till the competition of the Project (Likely to be completed till Dec 2019)	Initial Salary is Rs. 20000/- for 1 st year. Enhancement in salary as per guidelines of project.

Note: Candidate with relevant experience to the project may be considered for higher salary as per the project guidelines.

This post is purely on temporary basis till the completion of the project whichever is earlier, subject to satisfactory performance of candidate.

Tentative Date/Time and Venue for walk in interview: 24/09/2018 at 10:30 am in Institute Committee Room, Main Building.

Candidates are requested to send an advance soft copy of CV through e-mail to Chief- Investigator /Co-Chief Investigator.

Dr. R K Sarin,
Professor
Chief -Investigator / SMDP-C2SD,
ECE Department,
National Institute of Technology Jalandhar,
Punjab-144011
Mail id: sarinrk@nitj.ac.in

Dr. Ashish Raman,
Assistant Professor
Co-Chief Investigator / SMDP-C2SD,
ECE Department,
National Institute of Technology Jalandhar,
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No TA/DA shall be given for the interview. The selected candidates will be required to join immediately.

Information to the candidates

Duties and responsibilities for Lab Engineer and Project/Research Associate:

- Installing and maintaining CAD tools on the server.
- Guiding the UG/PG students on their project work in VLSI design & testing.
- Undergoing training on the advanced VLSI design topics under the Learning Enhancement programme (LEP) to be conducted at the VLSI man power resource centers.
- Preparation and submission of project reports to the project funding and monitoring agencies
- Developing the skills on advanced VLSI CAD tools and guide the students on chip design under SMDP-C2SD.
- Any other duties that may be assigned by the project coordinator and co-coordinator.
- Installing and Maintaining CAD tools in Windows and Linux Environment.
- Designing the proposed prototype of the project.