# Profile Page



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|---|---|---|
| Name | : | Dr Lalatendu Behera |
| Designation | : | Assistant Professor Grade-ii |
| Department | : | Computer Science & Engg. |
| Qualification | : | Ph.D. Computer Science and Engineering (IIT Guwahati) |
| | | M.Tech. Computer Science (NIT Rourkela) |
| | | B.E. Computer Science and Engineering (Utkal University, Bhubaneswar) |
| Address | : | Department of Computer Science and Engineering |
| | | NIT Jalandhar |
| | | Jalandhar, Punjab - 144011 |
| Email | : | beheral[at]nitj[dot]ac[dot]in |
| Phone | : | 0181-5037608 (Extn - 2518) |

## Research Interests :

Scheduling theory, Real-time and safety-critical system design, Mixed-criticality systems

## Other Profile Links :

**Google Scholar Link :**

Lalatendu  Click Here

**Personal Web Link :**

Personal website  Click Here

## Journal Publications :

| Year | Journal | Publication |
|---|---|---|
| 2021 | Computing, Springer, 104 (3), 577-599 | Lalatendu Behera, A fault-tolerant time-triggered scheduling algorithm of mixed-criticality systems |
| 2019 | Design Automation for Embedded Systems, 24 (2), 79-109 | L. Behera and P. Bhaduri, An energy-efficient time-triggered scheduling algorithm for mixed-criticality systems |
| 2017 | ACM Transactions on Design Automation of Electronic Systems, Vol. 22, Issue 4, 74.1 - 74.25 | L. Behera and P. Bhaduri, Time-Triggered Scheduling of Mixed-Criticality Systems |

## Conference Publications :

| Year | Conference | Publication |
|---|---|---|

| 2022 | International Conference on Distributed Computing and Internet Technology, LNCS, Vol. 13145 | Lalatendu Behera, MCDPS: An Improved Global Scheduling Algorithm for Multiprocessor Mixed-Criticality Systems |
|---|---|---|
| 2018 | International Conference on Distributed Computing and Internet Technology, LNCS, Vol. 10722 | L. Behera and P. Bhaduri, Time-Triggered Scheduling for Multiprocessor Mixed-Criticality Systems |

## Events Organized :

| Category | Type | Title | Venue | From | To | Designation |
|---|---|---|---|---|---|---|
| STC | International | Formal Verification of Cyber-physical Systems: Opportunities and Challenges | Department of CSE, NIT Jalandhar | 11-09-2020 | 15-09-2020 | Coordinator |
| FDP | International | Cloud Computing and Its Applications: Opportunities and Challenges | Department of CSE, NIT Jalandhar | 16-12-22 | 20-12-22 | Coordinator |

## Professional Affiliations :

| Designation | Organization |
|---|---|
| Member | IEEE |

## PhD Supervised :

| Scholar Name | Research Topic | Status | Year | Co-Supervisor |
|---|---|---|---|---|
| Ms. Harshita Singh | Real-time Scheduling Theory | Course Work Completed | 2022 | |
| Mr. Rishi Muni Pandey | Real-time Scheduling Theory | Course Work | 2022 | |
| Ms. Priyanka Mitra | Scheduling in Mixed-criticality Systems | Course Work Completed | 2021 | |
| Mr. Sreenu Banoth | Real-time Scheduling Theory | Course Work Completed | 2020 | |
| Mr. Manish Kumar Sharma | Image Processing using Machine Learning | Course Work Completed | 2020 | |

## PG Dissertation Guided :

| Student Name | Dissertation Title | Status | Year | Co-Supervisor |
|---|---|---|---|---|
| Mr. Prasoon Singh | Yet to be decided | Continuing | 2023 | |
| Ms. Shefali Bajaj | Yet to be decided | Continuing | 2023 | |
| Mr. Shivam Chauhan | Yet to be decided | Continuing | 2023 | |
| Mr. Rishi Muni Pandey | Non-preemptive Scheduling Algorithm for Mixed-criticality Systems | Completed | 2022 | |
| Ms. Aditi Anand | Indian Stock Trend Forecasting using Sentiment Analysis and Deep Neural Networks | Completed | 2022 | |

| Mr. Lalit Kumar Singh | A fault-tolerant time-triggered scheduling algorithm for mixed-criticality systems | Completed | 2021 | |
|---|---|---|---|---|
| Mr. Anurag Anand | An energy-efficient time-triggered scheduling algorithm for multiprocessor mixed-criticality systems | Completed | 2021 | |