SCRUTINIZING SHEET - Admissions (2020-2021) (Session July 2020)

Department Electronics and Communication Engineering

Course M.Tech (ECE/ VLSI)

Category GENERAL

S. No.	Application No., Name of Applicant, Father's Name	M.Tech Programme	Detail of Score in GATE Examination	Shortcomings in the application	Whether eligible for admission or not? Yes/No	Remarks
1.	20011050 ANKITA SHARMA D/o Sh. ANOOP SHARMA	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
2.	20011112 ANAMIKA SHARMA D/o Sh. ARVIND KUMAR SHARMA	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
3.	20011131 JYOTI SHARDA D/o Sh. SUNIL KUMAR SHARDA	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
4.	20011132 AMARAH ZAHRA D/o Sh. SYED MASOOD AHMAD	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

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Department Electronics and Communication Engineering

5.	20011150 FARHANA SHAHID D/o Sh. GH NABI SHAHEEN	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
6.	20011162 ADIL KHAN S/o Sh. NAHIF KHAN	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
7.	20011231 ADITI BHARMAIK D/o Sh. MOHAN BHARMAIK	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
8.	20011233 MOHIT SRIVASTAVA S/o Sh. DIWAKAR LAL SRIVASTAVA	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
9.	20011246 HARSHIT SRIVASTAVA S/o Sh. DIWAKAR LAL SRIVASTAVA	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents.* The candidate made provisionally eligible only based upon the information provided through online application
10.	20011249 AANCHAL SHARMA D/o Sh. RAM LAL SHARMA	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

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Department Electronics and Communication Engineering

11.	20011267 ADITI SHARMA D/o Sh. RAMESH SHARMA	Electronics and Communication Engineering	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
12.	20011039 SHIVALI D/o Sh. RAJ KUMAR	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
13.	20011058 PEEYUSH BHATT S/o Sh. GIRIJA KISHORE BHATT	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
14.	20011063 YASH WALIA S/o Sh. MANOJ WALIA	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
15.	20011086 MANISHIKA TRIPATHI D/o Sh. RAJENDRA TRIPATHI	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
16.	20011103 NEHA KUMARI D/o Sh. RAJ KAPOOR	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
17.	20011125 ESHA AGARWAL D/o Sh. RAJ UMAR AGARWAL	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

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Department Electronics and Communication Engineering

18.	20011137 ASHISH PRASHAR S/o Sh. OM PRAKASH	VLSI Design	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
19.	20011159 AITRAIYEE KONAR D/o Sh. SUMIT KONAR	VLSI Design	No	result Awaited	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
20.	20011181 ADAMYA SHUKLA S/o Sh. SUDHANSHU SHEKHAR SHUKLA	VLSI Design	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
21.	20011185 SOHAM DAS S/o Sh. MAHADEV CHANDRA DAS	VLSI Design	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
22.	20011303 KSHITIZ RATHORE S/o Sh. RAJENDRA SINGH	VLSI Design	No	no	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

** The candidates whose result of qualifying is awaited, they shall be given provisional admission after an undertaking that they shall submit the result upto 30th October 2020, failing which their admission shall automatically stand cancelled.

Dr Nitesh Kashyap

Dr Sateesh K. Awasthi Dr Pawan K. Verma

Dr Sukwinder Singh

Dr Tarun Chaudhary Dr Manjeet Singh

Dr Ramesh K. Sunkaria HOD/ECE Dr Mamta Khosla

Dr B. S. Saini Chairman

SCRUTINIZING SHEET - Admissions (2020-2021) (Session July 2020)

Department Electronics and Communication Engineering

Course M.Tech (ECE/ VLSI) Category OBC

S. No.	Application No., Name of Applicant, Father's Name	M.Tech Programme	Detail of Score in GATE Examination	Shortcomings in the application	Whether eligible for admission or not? Yes/No	Remarks
1.	20011022 SOURABH KUMAR VERMA S/o Sh. RAJENDRA KUMAR VERMA	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
2.	20011144 VAISHNAWI SINGH D/o Sh. MAHENDRA KUMAR SINGH	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
3.	20011294 YUVRAJ DEWANGAN S/o Sh. NIRANJAN LAL DEWANGNA	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online

SCRUTINIZING SHEET - Admissions (2020-2021) (Session July 2020)

Department Electronics and Communication Engineering

					application
4.	20011035 NAV PRIYA BHARTI D/o Sh. BIDYA BHUSHAN SINGH	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
5.	20011096 SANJAY YADAV S/o Sh. ASHOK KUMAR YADAV	VLSI Design	No	Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

** The candidates whose result of qualifying is awaited, they shall be given provisional admission after an undertaking that they shall submit the result upto 30th October 2020, failing which their admission shall automatically stand cancelled.** OBC Certificate issued on or after 01 April 2020 is required.

Dr Nitesh Kashyap	Dr Sateesh K. Awasthi	Dr Pawan K. Verma	Dr Sukwinder Singh	Dr Tarun Chaudhary	Dr Manjeet Singh
Dr Ramesh K. Sunka HOD/ECE	ia Dr Ma	ımta Khosla	Dr B. S. Saini Chairman		

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Department Electronics and Communication Engineering

Course M.Tech (ECE/ VLSI)

Category SC

S. No.	Application No., Name of Applicant, Father's Name	M.Tech Programme	Detail of Score in GATE Examination	Shortcomings in the application	Whether eligible for admission or not? Yes/No	Remarks
1.	20011036 SUSHEEL KUMAR ARYA S/o Sh. ADARSH KUMAR ARYA	VLSI Design	EC20S48015060 (2020) Score: 279		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

** The candidates whose result of qualifying is awaited, they shall be given provisional admission after an undertaking that they shall submit the result upto 30th October 2020, failing which their admission shall automatically stand cancelled.

Dr Nitesh Kashyap	Dr Sateesh K. Awasthi	Dr Pawan K. Verma	Dr Sukwinder Singh	Dr Tarun Chaudhary	Dr Manjeet Singh
Dr Ramesh K. Sunka	ria Dr Ma	mta Khosla	Dr B. S. Saini		

HOD/ECE

Chairman

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Department Electronics and Communication Engineering

Course M.Tech (ECE/ VLSI)

Category EWS

S. No.	Application No., Name of Applicant, Father's Name	M.Tech Programme	Detail of Score in GATE Examination	Shortcomings in the application	Whether eligible for admission or not? Yes/No	Remarks
1.	20011281 RAHUL TIWARI S/o Sh. RAJESHWAR TIWARI	Electronics and Communication Engineering	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
2.	20011202 NITISH KUMAR S/o Sh. AJAY KUMAR SINGH	VLSI Design	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application
3.	20011318 RAHUL TIWARI S/o Sh. RAJESHWAR TIWARI	VLSI Design	No		Provisionally Eligible	*Provisionally Eligible subject to production and verification of all necessary documents. * The candidate made provisionally eligible only based upon the information provided through online application

** The candidates whose result of qualifying is awaited, they shall be given provisional admission after an undertaking that they shall submit the result upto 30th October 2020, failing which their admission shall automatically stand cancelled. ** EWS Certificate issued on or after 01 April 2020 is required.

Dr Nitesh Kashyap Dr Sateesh K. Awasthi Dr Pawan K. Verma Dr Sukwinder Singh Dr Tarun Chaudhary Dr Manjeet Singh Dr Ramesh K. Sunkaria Dr Mamta Khosla Dr B. S. Saini HOD/ECE Chairman