ORGANIZING COMMITTEE

Chief Patron
Professor Lalit Kumar Awasthi
Director, NIT Jalandhar

Convener
Dr. Ramesh Kumar Sunkaria
Head of department
ECE, NIT Jalandhar

Coordinator(s)
Dr. Tarun Chaudhary
Assistant Professor
Department of ECE

Dr. Manjeet Singh
Assistant Professor
Department of ECE

REGISTRATION FORM

Dr B R Ambedkar
National Institute of Technology
Jalandhar - 144011, Punjab

Short Term Course
on
Low Power VLSI Design for
Communication Systems and
Networks (LVCSN’20)

(September 16th – 20th ,2020)
(One Week Duration)

HOW TO APPLY
Registration can be done using
Google form link below:
https://tinyurl.com/y2gcbal8

Important Dates:
LAST DATE OF REGISTRATION
15.09.2020

REPORTING DATE AND TIME
16.09.2020
11:00 AM

No Registration fees

TEQIP-III Sponsored

Short Term Course on
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Organized by
Department of Electronics and
Communication

Dr B R Ambedkar National Institute of Technology Jalandhar – 144 011, Punjab

www.nitj.ac.in
ABOUT NIT JALANDHAR
Dr. B. R. Ambedkar National Institute of Technology, Jalandhar was established in the year 1987 as Regional Engineering College and attained the status of National Institute of Technology on October 17, 2002. As National Institute of Technology, the Institute has a responsibility of providing high quality technical education in Engineering and Technology to produce competent technical manpower for the country. The Institute offers B. Tech. Programmes in twelve disciplines of Engineering and Technology along with the Research Programmes leading to M. Sc., M. Tech. and Ph. D. degrees. The Institute has signed Memorandum of Understanding (MoU) with many prestigious Institutes such as Ecole Centrale de Lille, France, University of Johannesburg, South Africa, along with other Universities abroad including UK, USA and Canada for the mutual academic exchange program and further strengthening of the academics and research.

ABOUT THE DEPARTMENT
The Department of Electronics and Communication Engineering offers B. Tech. in Electronics and Communication Engineering, two full time MTech program in Electronics and Communication Engineering and VLSI Design. Department also offers the Doctoral programs in the field of Communication, VLSI Design and Bio-Medical Engineering. The department has established state of-art laboratories with sophisticated equipment’s for undergraduate, post graduate and Ph.D. research work. The department has an advanced VLSI lab sponsored by Ministry of Communication and IT under SMDP-C2SD and Medical Imaging and Computational Modelling of Physiological Systems Lab. In the VLSI lab Cadence, Mentor graphic, Synopsys, and Xilinx softwares are available for synthesis /simulation work.

OBJECTIVE OF THE COURSE
Low Power VLSI Design has significantly evolved modern electronic industry and state of the art system design. There is of great need for manpower training in the area of nanoelectronics for integrated technology. This STC also defines critical problems of electronics system design with reduced power in the field of communication systems and networking area. The recent topics related to blockchain and IoT will also be covered to enhance the knowledge of participants in these research areas. During this programme the scientists, academicians and industry persons in their respective areas of expertise will discuss the current scenario on novel semiconductor devices and their applications used in current trends of research in communication and others field as well. Our experts will also share their views regarding latest communication techniques and their advanced applications.

IMPORTANT DATES
Last Date of Registration: 15/09/2020
VENUE: Google meet

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