# Online Short Term Course (e-STC)

# Research Trends in VLSI for Communication Systems



ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT DR B R AMBEDKAR NATIONAL INSTITUTE OF TECHNOLOGY JALANDHAR

23th to 27th November 2021

Registration link:- https://www.nitj.ac.in/events\_registration/stc\_rtvlsi/login Register by November 22, 2021 by 12:30 PMz

### Objective of the STC

VLSI, Nano-Electronics and Communication System design have evolved significantly modern electronic and communication systems design. There is a great need of manpower training in the area of Nano-Electronics and Communication System Design. The main objective of this course is to provide theoretical and practical knowledge of the recent trends available in the domain of communication and Nano-Electronics applied to signal design. During this program the scientists and academicians in their respective areas of expertise will discuss the current scenario on VLSI and antenna Design and their applications in Communication systems.



### Chief Guest and key-note Speaker

## Prof. Rajesh Khanna

**Thapar University** 

#### **Research Interests**

- SDR
- Cognitive Radio
- MIMO
- Antenna design

#### Contents

- Theoretical and practical knowledge of the recent trends available in the domain of VLSI and Nano-electronics for communication system.
- Antennas for modern communication system

# **General Information:**

The candidate will be informed of his/her selection in advance.

Rs 100/- +18 % GST Course fee will be charged from participants.

E-Certificate will be issued to participants (Attendance is compulsory for all participants)

### **Organizing Team**

#### **PATRON**

#### Professor R K Garg Director

Dr B R Ambedkar National Institute of Technology Jalandhar

#### **HEAD OF THE DEPT.**

Dr. Mamta Khosla

Dept. of Electronics & Communication Engineering
Dr B R Ambedkar National Institute of Technology

# CONVENER

Jalandhar

Dr. Nitesh Kashyap

#### **COORDINATORS**

Dr. Mamta Khosla

Dr. Ashish Raman

Dr. Deepti Kakkar



#### **CONTACT PERSON**

#### Dr. Nitesh Kashyap

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### About the Institute

Dr B R Ambedkar National Institute of Technology was established in the year 1987 as Regional Engineering College and was given the status of National Institute of Technology (Deemed University) by the Government of India on October 17, 2002 under the aegis of Ministry of Human Resource Development, New Delhi. Now the Ministry of Human Resource Development, Govt of India has declared the Institute as "Institute of National Importance" under the act of Parliament -2007. A large number of reputed Industrial houses in the country visit the Institution and select the final year students as Engineers/Management Trainees. As one of the National Institutes of Technology (NIT), the Institute has the responsibility of providing high quality



education in Engineering, Technology and Sciences to produce competent technical and scientific manpower for the country. The Institute offers BTech, MTech, MSc, MBA and PhD programmes in the several disciplines of Engineering,

## About the Department

The Department of Electronics & Communication Engineering, Dr. B R Ambedkar NIT Jalandhar has been offering B.Tech programme in Electronics & Communication Engineering since 1989, M.Tech programme in Electronics & Communication Engineering (Full-Time) and VLSI Design (Full Time) from year 2007. The Department also offers Ph.D. programmes in various specializations, such as Biomedical Engineering, VLSI Design, Nanoelectronics and Communication Systems etc. The Department has presently 15 faculty members with PhD degrees from reputed Institutes. The Department has established state-of-the-art laboratories for B.Tech, M.Tech and Ph.D. research work. The Department has advanced Biomedical Signal Processing and Telemedicine Laboratory, VLSI Design Laboratory, Nanoelectronics Research Lab and Communication System Laboratory with sophisticated research equipment. The department has an advanced VLSI lab sponsored by Ministry of Communication and IT under SMDP-II project. In the VLSI lab Cadance, Mentor graphic, Synopsys, Magma, Coware and Xilinx softwares are available for project/research work

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